

EL465780142US

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22

**DECLARATION OF SOLE INVENTOR FOR PATENT APPLICATION**

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: Memory Circuitry and Method of Forming Memory Circuitry.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations §1.56.

**PRIOR FOREIGN APPLICATIONS:**

I hereby state that no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

Direct all communications to Mark S. Matkin, at WELLS, ST. JOHN, ROBERTS, GREGORY & MATKIN P.S., 601 W. First Avenue, Suite 1300, Spokane, WA 99201-3828. Telephone: (509) 624-4276; (PTO Customer No. 021567).

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and

1 belief are believed to be true; and further that these statements were  
2 made with the knowledge that willful false statements and the like so  
3 made are punishable by fine or imprisonment, or both, under  
4 Section 1001 of Title 18 of the United States Code and that such willful  
5 false statement may jeopardize the validity of the application or any  
6 patent issued therefrom.

7 \* \* \* \* \*

8 Full name of sole inventor: Belford T. Coursey

9 Inventor's Signature: 

10 Date: 8/22/2000

11 Residence: Meridian, Idaho

12 Citizenship: USA

13 Post Office Address: 1194 S. Rollinghills Drive  
14 Meridian, ID 83642  
15  
16  
17  
18  
19  
20  
21  
22